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Date: May 24, 2004
To Name: USPTO – Box: Status Request
Fax: (703) 872-9306
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From: Name: Manu Kashyap, Intellectual Property Paralegal
Corporate Legal Dept.
Telephone: (408) 433-7475
Fax: (408) 433-7460
Re: 10/021,696

Number of Pages Including this Page 3

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MESSAGE:

US Serial No.: 10/021,696

Filing Date: October 30, 2001

Group Art Unit: 2825

Docket No: 01-490

Examiner: Vuthe Siek

Request for Status

Please notify us immediately if any pages are not received.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED
CENTRAL FAX CENTERMAY 22 2004
OFFICIALIn re Application of :

Michael Eneboe et. al.

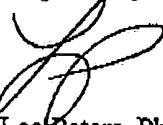
Serial No. : 10/021,696Group Art Unit : 2825Filed : October 30, 2001Examiner : Siek, VutheFor : System and Method for Optimizing
an Integrated Circuit DesignAtty Docket : / 01-490STATUS REQUEST

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,


Leo Peters, Ph: [+1] 408-433-7475

Reg. No. 33,562

Date: 5-24-04